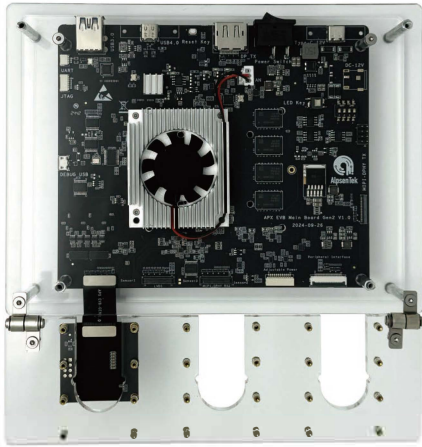




HybridVision® APX EVK Gen2

The latest board-level development platform for Hybrid Vision



Multi-sensor
Compatibility



Full Access
to SDK



Variety of
Interfaces



Sets of Product
Documentation

The APX EVK Gen2 incorporate an APX hvs sensor module, a robust FPGA, data storage units, power management unit and various data interfaces, equipped with firmware, software, and algorithmic functions to expedite user-driven development. It offers the flexibility and extendable solutions required for rapid setup, along with substantial processing power for experimenting with various product development strategies.

Key Specification

Dimensions	L200 × W150 × H45 mm	Operating voltage	Analog: 1.2-4.0V
Sensor Compatibility	APX HVS Sensors		Digital: 0.6-1.9V(DVDD1-3), 1.2-4V(DVDD4-5)
Power interface	Type-C DC12V		Bias: 0-1.8V (8lane)
FPGA	BlockRAM: 4.5Mb	Output interface	MIPI C-PHY V1.2, MIPI D-PHY V1.2
	UltraRAM: 13.5Mb		DVP, QSPI, SPI, LVDS
	CLB LUTS: 87.8k	PC communication interface	USB3.0, USB4.0 & Thunderbolt 4.0
Debug interface	JTAG, UART	Display port	DP1.2
Control Interface	I2C, I3C, SPI	Primary boot memory	QSPI Norflash 64M
Extended interface	PIR/IMU/Flashlight/VCSEL/Fill-light	Secondary boot memory	SDHC card
DDR memory	4GB 64-bit DDR4	eMMC memory	EMMC4.5, 16GB

*The specification are subject to the final product manual. Please contact us for more information if you are looking for a substantial evaluation board for the innovative hybrid vision sensors.

Shenzhen | Zurich | Beijing | Nanjing

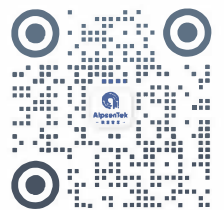
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